## USB High-Side Power Switch

## FEATURES

■ +3.0 V to +5.5 V Input Voltage Range
■ 500mA Continuous Load Current per Channel
■ 2.6V Undervoltage Lockout
■ 1.25A Short Circuit Current Limit

- $100 \mathrm{~m} \Omega$ Maximum On-Resistance

■ $75 \mu \mathrm{~A}$ On-State Supply Current
■1uA Shutdown Current
■ Output can be Forced Higher than Input (Off-State)

- Thermal Shutdown

■ Slow Turn On and Fast Turn Off


Now Available in Lead Free Packaging
Active-High Version: SP2525A-1
■ Active-Low Version: SP2525A-2
APPLICATIONS

- USB Power Management
- Hot Plug-In Power Supplies
- Battery Charger Circuit

The SP2525A device is an integrated high-side power switch ideal for self-powered and buspowered Universal Serial Bus (USB) applications. The high-side switches are MOSFETs with $70 \mathrm{~m} \Omega$ RDs(on), which meets USB voltage drop requirements for maximum transmission wire length. Multi-purpose open-drain fault flag output indicates over-current limiting, thermal shutdown, or undervoltage lockout. Output current is typically limited to 0.85A below the 5A safety requirement, and the thermal shutdown function shuts the switch off to prevent damage under overcurrent conditions. Guaranteed minimum output rise time limits inrush current during hot plug-in, minimizing EMI and preventing the voltage at the upstream port from dropping excessively.


One Port Self Powered Hub

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications below is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

| Supply Voltage( $\mathrm{V}_{\mathrm{IN}}$ )....................................7.0V | Operating Temperature Range............ $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| :---: | :---: |
| Fault Flag Voltage ( $\mathrm{VFLG}^{\text {F }}$ ).............................7.0V | Storage Temperature Range............. $65^{\circ} \mathrm{C}$ to $+150^{\circ} \mathrm{C}$ |
| Fault Flag Current ( $\mathrm{I}_{\mathrm{FLG}}$ )............................. 50 mA | Power Dissipation Per Package |
|  | 8-pin NSOIC <br> (derate $6.14 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $+70^{\circ} \mathrm{C}$ ) $\qquad$ |

Unless otherwise noted, the following specifications apply for $\mathrm{V}_{\text {IN }}=+5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$.

| PARAMETER | MIN. | TYP. | MAX. | UNITS | CONDITIONS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Supply Current |  | $\begin{gathered} 0.75 \\ 75 \end{gathered}$ | $\begin{aligned} & 5.0 \\ & 100 \end{aligned}$ | $\mu \mathrm{A}$ | $\begin{aligned} & \mathrm{V}_{\text {EN }}=\text { Logic "0" OUT }=\text { Open } \\ & \mathrm{V}_{\text {EN }}=\text { Logic "1" OUT }=\text { Open } \end{aligned}$ |
| Enable Input Voltage | 2.4 | $\begin{aligned} & 1.7 \\ & 2.0 \end{aligned}$ | 0.8 | V | $\begin{aligned} & \mathrm{V}_{\mathrm{EN}}=\text { Logic "0" } \\ & \mathrm{V}_{\mathrm{EN}}=\text { Logic "1" } \end{aligned}$ |
| Enable Input Current |  | $\begin{aligned} & 0.01 \\ & 0.01 \end{aligned}$ | $\begin{aligned} & 1 \\ & 1 \end{aligned}$ | $\mu \mathrm{A}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{EN}}=\text { Logic "0" } \\ & \mathrm{V}_{\mathrm{EN}}=\text { Logic "1" } \end{aligned}$ |
| Enable Input Capacitance |  | 1 |  | pF |  |
| Output Mosfet Resistance |  | 70 | 100 | $\mathrm{m} \Omega$ |  |
| Output Turn-On Delay |  | 100 |  | $\mu \mathrm{s}$ | R $\mathrm{L}=10 \Omega$ each output |
| Output Turn-On Rise Time |  | 1000 | 4000 | us | RL=10 $\Omega$ each output |
| Output Turn-Off Delay |  | 0.8 | 20 | $\mu \mathrm{s}$ | RL=10 $\Omega$ each output |
| Output Turn-Off Fall Time |  | 0.7 | 20 | $\mu \mathrm{s}$ | RL=10 $\Omega$ each output |
| Output Leakage Current |  |  | 10 | $\mu \mathrm{A}$ |  |
| Current Limit Threshold | 0.6 | 1.0 | 1.25 | A |  |
| Over-Temperature Shutdown Threshold |  | $\begin{aligned} & 135 \\ & 125 \end{aligned}$ |  | ${ }^{\text {o }}$ | T, increasing <br> $\mathrm{T}_{\mathrm{j}}$ decreasing |
| Error Flag Output Resistance |  | $\begin{aligned} & 10 \\ & 15 \end{aligned}$ | $\begin{aligned} & 25 \\ & 40 \end{aligned}$ | $\Omega$ | $\begin{aligned} & \mathrm{V}_{\text {IN }}=5 \mathrm{~V}, \mathrm{IL}=10 \mathrm{~mA} \\ & \mathrm{~V}_{\mathbb{I N}}=3.3 \mathrm{~V}, \mathrm{I}_{\mathrm{L}}=10 \mathrm{~mA} \end{aligned}$ |
| Error Flag Off Current |  | 0.01 | 1 | $\mu \mathrm{A}$ | $\mathrm{V}_{\text {FLAG }}=5 \mathrm{~V}$ |
| UVLO Threshold |  | $\begin{aligned} & 2.6 \\ & 2.4 \end{aligned}$ |  | V | $\mathrm{V}_{\text {IN }}$ increasing <br> $\mathrm{V}_{\text {IN }}$ decreasing |


| Pin Number | Pin Name | Description |
| :---: | :---: | :--- |
| 1 | EN | Enable Input Active High for SP2525A-1 and Active Low for SP2525A-2. |
| 2 | FLG | An active-low and open-drained fault flag output for power switch. It can <br> indicate current limit if CTL is active. In normal mode operation, it also can <br> indicate thermal shutdown or undervoltage. |
| 3 | GND | Chip power ground. |
| 4 | NC | Not internally connected. |
| 5,7 | IN | Power supply input. |
| 6,8 | OUT | MOSFET switch output. |

TEST CIRCUIT


BLOCK DIAGRAM


## Error Flag

The Error Flag is an open-drained output of an N-channel MOSFET, the FLG output is pulled low to signal the following fault conditions: input undervoltage, output current limit, and thermal shutdown.

## Current Limit

The current limit threshold is preset internally. It protects the output MOSFET switches from damage resulting from undesirable short circuit conditions or excess inrush current, which is often encountered during hot plug-in. The low limit of the current limit threshold of the SP2525A allows a minimum current of 0.5 A through the MOSFET switches. A current limit condition will signal the Error Flag.

## Thermal Shutdown

When the temperature of the SP2525A exceeds $135^{\circ} \mathrm{C}$ for any reasons, the thermal shutdown function turns off the MOSFET switch and signals the Error Flag. A hysteresis of $10^{\circ} \mathrm{C}$ prevents the MOSFETs from turning back on until the chip temperature drops below $125^{\circ} \mathrm{C}$.

## Supply Filtering

A $0.1 \mu \mathrm{~F}$ to $1 \mu \mathrm{~F}$ bypass capacitor from IN to GND, located near the device, is strongly recommended to control supply transients. Without a bypass capacitor, an output short may cause ringing on the input (from supply lead inductance) which can damage internal control circuitry.

## Transient Requirements

USB supports dynamic attachment (hot plugin) of peripherals. A current surge is caused by the input capacitance of a downstream device. Ferrite beads are recommended in series with all power and ground connector pins. Ferrite beads reduce EMI and limit the inrush current during hot-attachment by filtering high-frequency signals.

## Short Circuit Transient

Bulk capacitance provides the short-term transient current needed during a hot-attachment event. A $33 \mu \mathrm{~F} / 16 \mathrm{~V}$ tantalum ora $100 \mu \mathrm{~F} /$ 10 V electrolytic capacitor mounted close to the downstream connector at each port should provide sufficient transient drop protection.

## Printed Circuit Layout

The Power circuitry of USB printed circuit boards requires a customized layout to maximize thermal dissipation and to minimize voltage drop and EMI.

## Input and Output

The independent solid state switch connects the IN pin to the OUT pin when enabled by a logic signal at EN. The IN pin is the power supply connection to the internal circuitry and the drain of the output MOSFET. The OUT pin is the source for the MOSFET. Typically, the current in USB application will flow through the switch from IN to OUT towards the load. If Vout is greater than VIN when a switch is enabled, the current will flow from OUT to the IN pin because the MOSFET channels are bidirectional when switched on. The output source is allowed to be externally forced to a higher voltage than its input without causing unwanted current flow when the output is disabled.


Output On-Resistance vs. Supply Voltage


UVLO Threshold Voltage vs. Temperature


On-state Supply Current vs. Temperature


Output On-Resistance vs. Temperature


On-state Supply Current vs. Supply Voltage

OFF State Supply Current vs. Supply Voltage


Off-state Supply Current vs. Supply Voltage
$\mathrm{V}_{\text {IN }}=+5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{AMB}}=+25^{\circ} \mathrm{C}$ unless otherwise noted. CTL Threshold Voltage vs. Supply Voltage


Control Threshold vs. Supply Voltage


Output Rise Time vs. Temperature


Response


Current Limit Threshold vs. Temperature


Output Fall Time vs. Temperature


Turn-On, Turn-Off Characteristics
$\mathrm{V}_{\mathrm{IN}}=+5.0 \mathrm{~V}, \mathrm{~T}_{\mathrm{AMB}}=+25^{\circ} \mathrm{C}$ unless otherwise noted.



| 8 Pin NSOIC |  |  | JEDEC MS-012 |  | Variation AA |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| SYMBOL | Dimensions in Millimeters: Controlling Dimension |  |  | Dimensions in Inches Conversion Factor: 1 Inch $=25.40 \mathrm{~mm}$ |  |  |
|  | MIN | NOM | MAX | MIN | NOM | MAX |
| A | 1.35 | - | 1.75 | 0.053 | - | 0.069 |
| A1 | 0.10 | - | 0.25 | 0.004 | - | 0.010 |
| A2 | 1.25 | - | 1.65 | 0.049 | - | 0.065 |
| b | 0.31 | - | 0.51 | 0.012 | - | 0.020 |
| c | 0.17 | - | 0.25 | 0.007 | - | 0.010 |
| E | 6.00 BSC |  |  | 0.236 BSC |  |  |
| E1 | 3.90 BSC |  |  | 0.154 BSC |  |  |
| e | 1.27 BSC |  |  | 0.050 BSC |  |  |
| h | 0.25 |  | 0.50 | 0.010 | - | 0.020 |
| L | 0.40 | - | 1.27 | 0.016 | - | 0.050 |
| L1 | 1.04 REF |  |  | 0.041 REF |  |  |
| L2 | 0.25 BSC |  |  | 0.010 BSC |  |  |
| R | 0.07 | - | - | 0.003 | - | - |
| R1 | 0.07 | - | - | 0.003 | - | - |
| $\varnothing$ | $0^{\circ}$ | - | $8^{\circ}$ | $0^{\circ}$ | - | $8^{\circ}$ |
| $\varnothing 1$ | 50 | - | $15^{\circ}$ | 50 | - | $15^{\circ}$ |
| $\varnothing 2$ | $0{ }^{\circ}$ | - | - | $0^{\circ}$ | - | - |
| D | 4.90 BSC |  |  | 0.193 BSC |  |  |
| SIPEX Pkg Signoff Date/Rev: |  |  |  | JL Aug16-05 / Rev A |  |  |

## ORDERING INFORMATION

| Part Number | Temperature Range | Package Types |
| :---: | :---: | :---: |
| SP2525A-1EN | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 -pin NSOIC |
| SP2525A-1EN/TR | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | .. 8 -pin NSOIC |
| SP2525A-2EN. | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 -pin NSOIC |
| SP2525A-2EN/TR | $-40^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ | 8 -pin NSOIC |

Available in lead free packaging. To order add "-L" suffix to part number.
Example: SP2525A-1EN/TR = standard; SP2525A-1EN-L/TR = lead free

## /TR = Tape and Reel

Pack quantity is 2,500 for 8 pin NSOIC.

Solved by Sipex

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